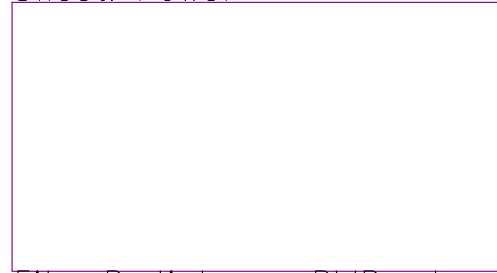
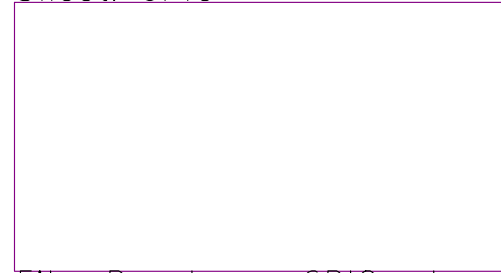


Sheet: Power



File: Radioberry-PWR.sch

Sheet: GPIO

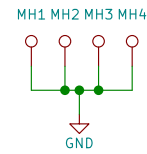


File: Raspberry-GPIO.sch

Sheet: Frontend



File: Radioberry-Frontend.sch



PA3GSB  
**AppMind**

Sheet: /  
File: Radioberry.sch

**Title: RadioBerry V2.0**

Size: A4 Date: 2021-04-21

KiCad E.D.A. kicad (5.1.8)-1

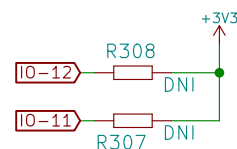
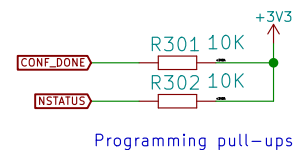
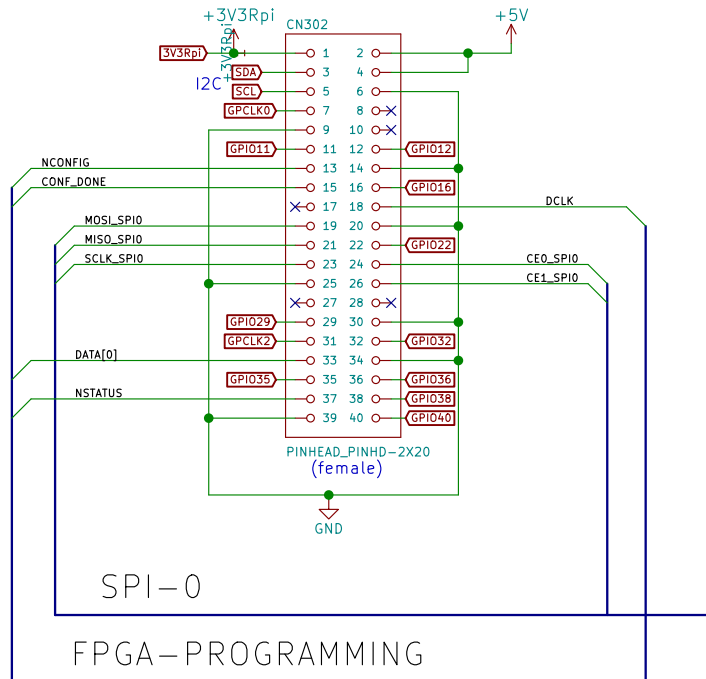
**Rev: beta5**

Id: 1/4

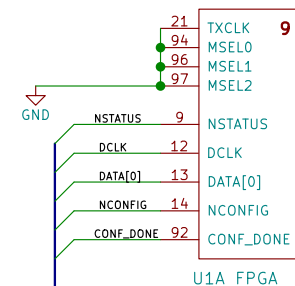
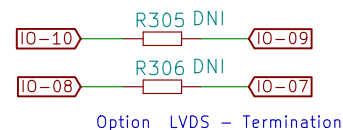
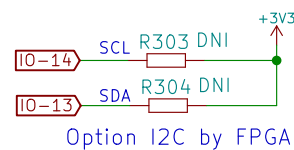


## Attach to RPI GPIO Header connector

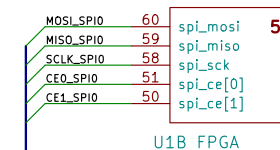
PWR\_FLAG  
+3V3Rpi  
+3V3Rpi



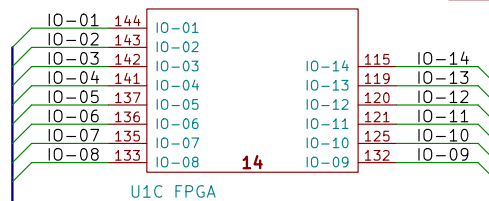
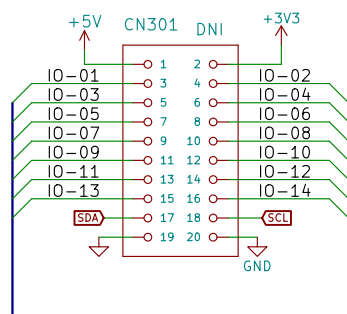
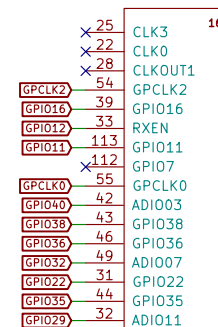
(internal FPGA pull up to large / avoiding speed problems)



## FPGA-PROGRAMMING



## SPI-0



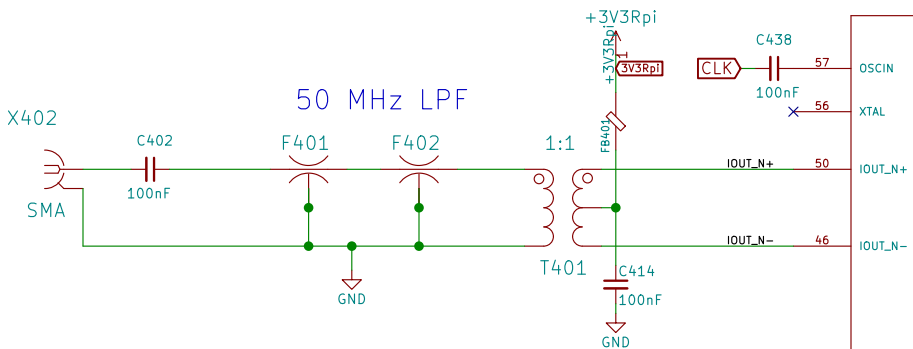
PA3GSB  
AppMind  
Sheet: /GPIO/  
File: Raspberry-GPIO.sch

## Title:

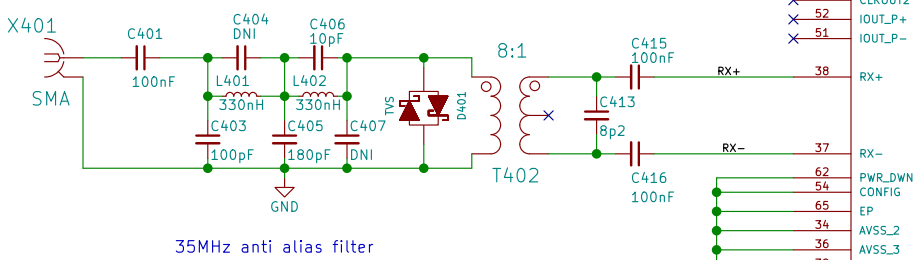
Size: A4 Date: 2021-04-21  
KiCad E.D.A. kicad (5.1.8)-1

Rev: beta5  
Id: 3/4

RF-OUT



RF-IN



AD9866

IC401

